

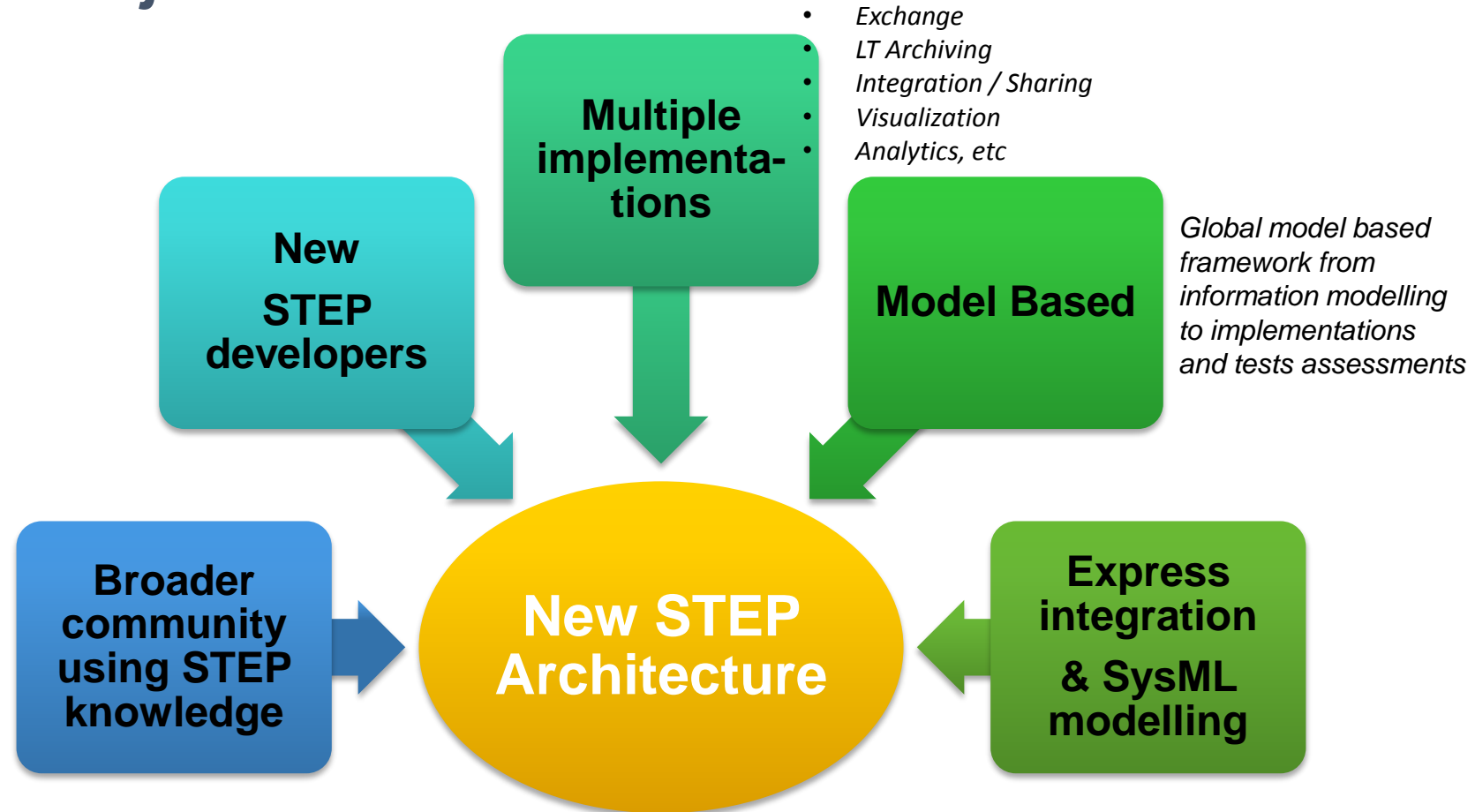


STEP New Architecture ASD-AIA presentation

Webex
Jan 24th , 2017

Jan 24, 2017

Objectives for new architecture

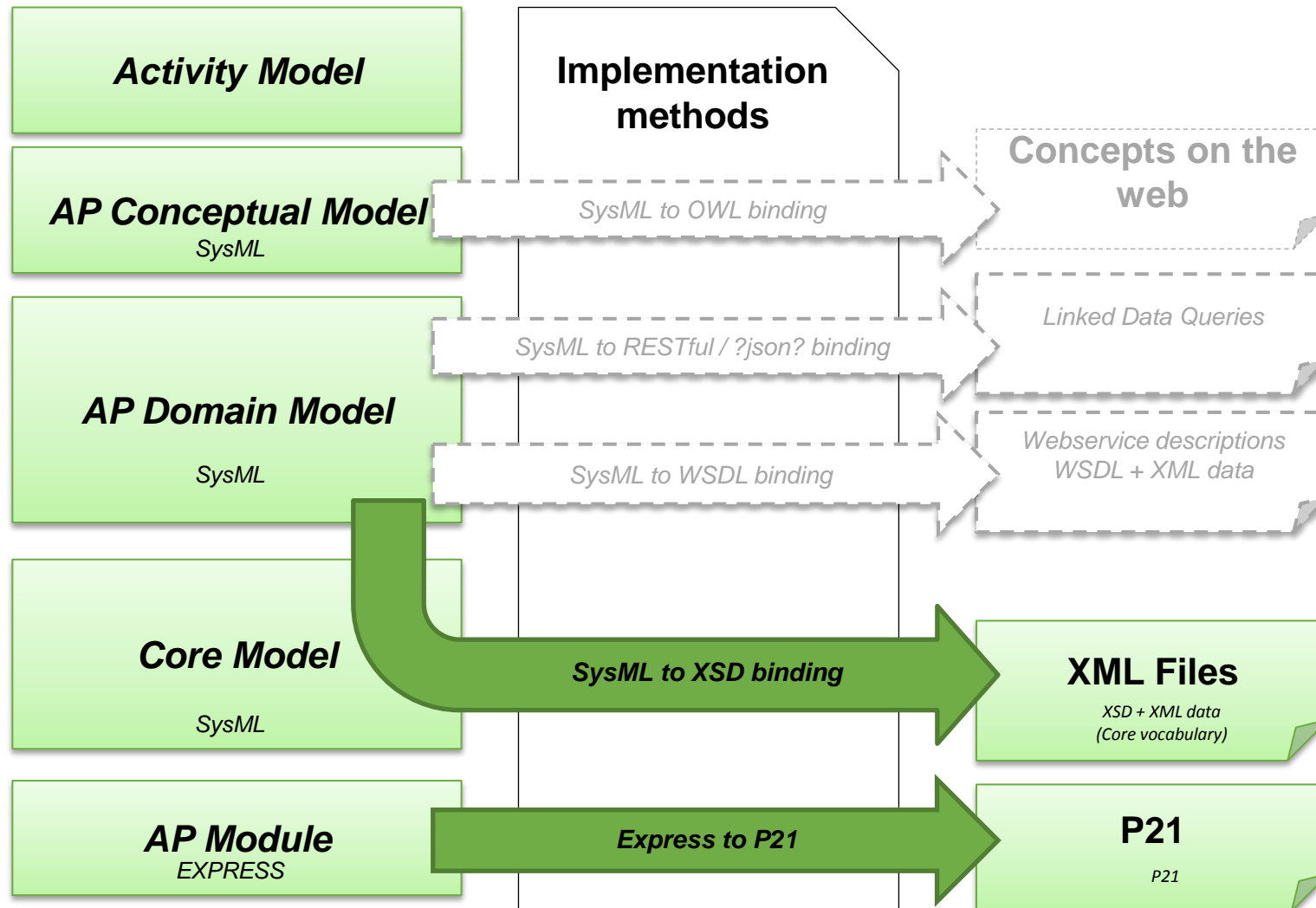


Scope of the new STEP architecture

- In Scope
 - Provide the development, publication, implementation and testing infrastructure for the development of the STEP standards
 - Enhance the methods for concurrent development, quality check, and consensus building , based on / Compliant with / the ISO procedures
 - Provide the containers and the methods to gather the standards requirements, schemas, definitions, tests , ...
- Out of Scope
 - The content of the STEP standards is not defined by the new STEP architecture. It is defined by the STEP development projects

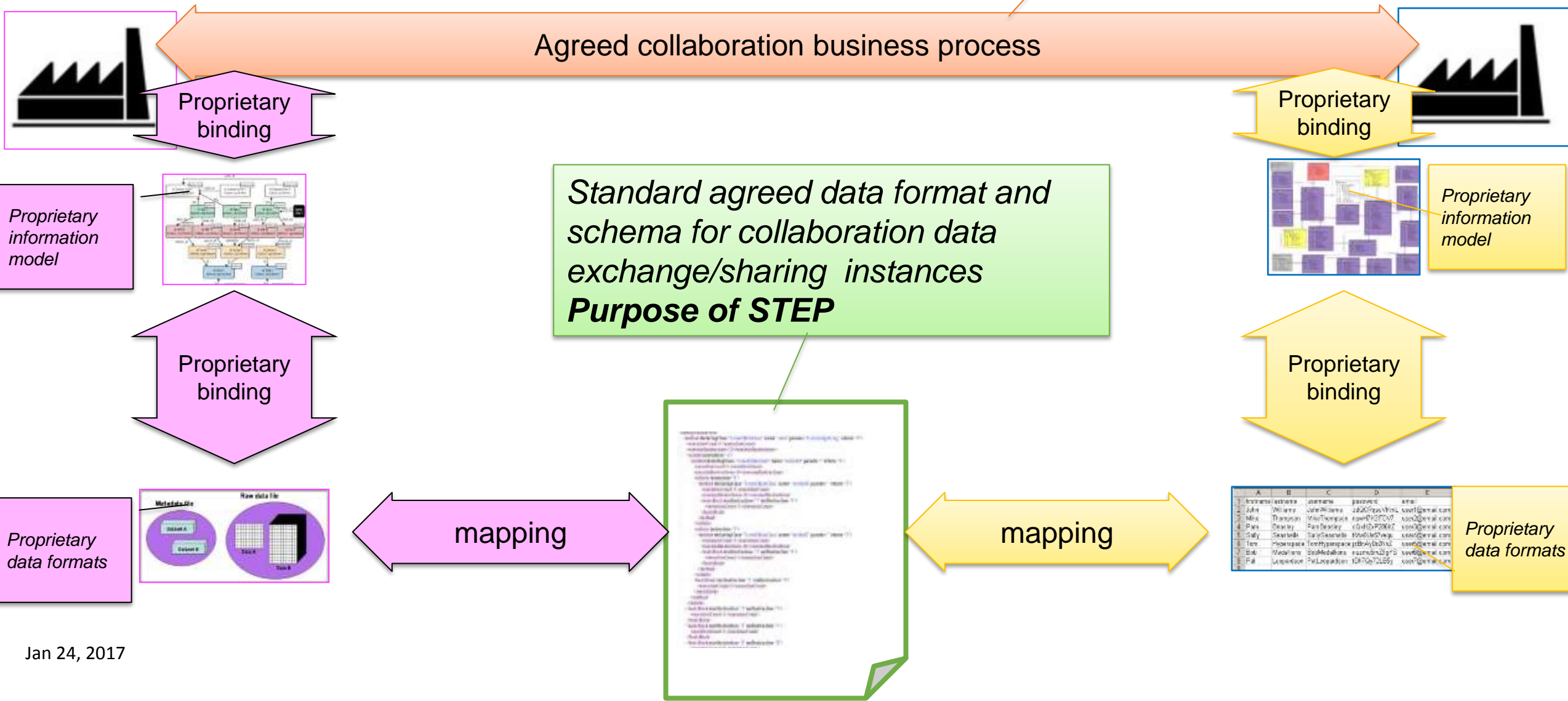
*NOTE: the seed of the STEP **core model** has been defined by the new STEP architecture initiative as **AP242 ed1 BO Model**, and all changes on that seed should be managed by the ISO process using the new STEP architecture artefacts*

STEP New Architecture is a multi-layers framework



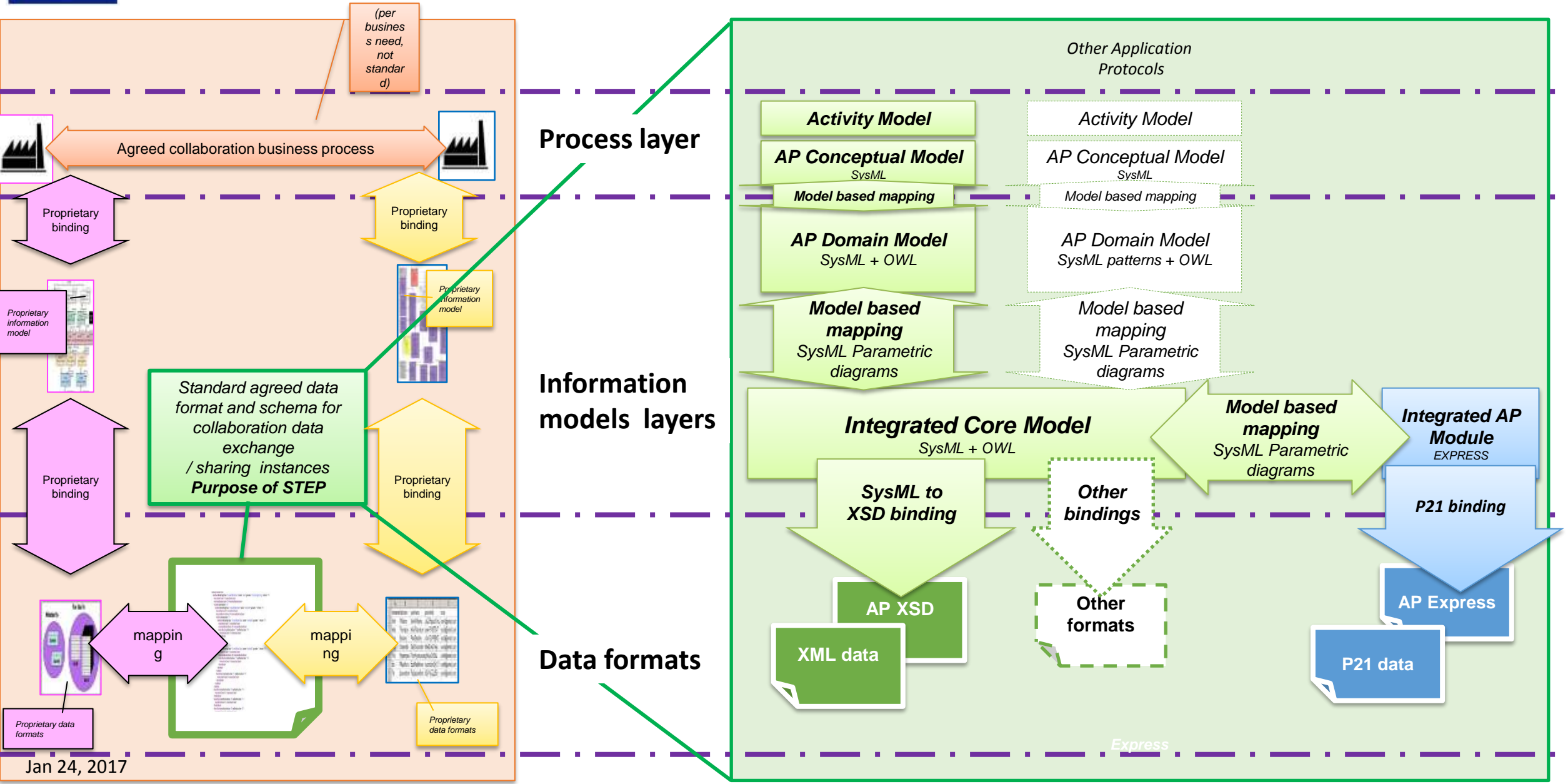
Interoperability need

(per business
need, not
standard)

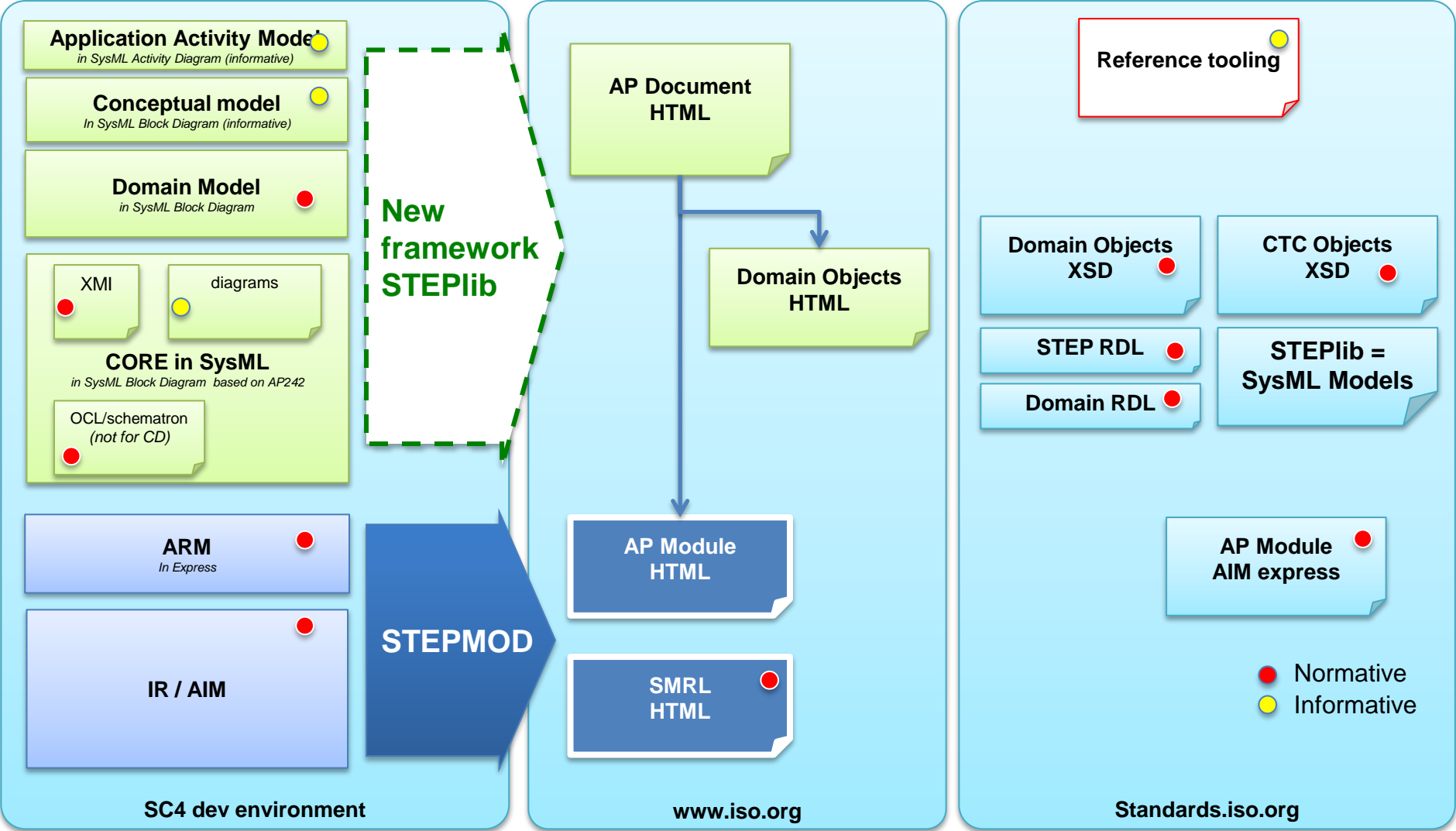




STEP New Architecture provide a model based framework to ensure that the standards developments meets the industry collaboration and integration business needs with long term preservation of investments .



Documentation scheme for publishing STEP AP



239 ed3

4.1 Activity Model
 4.2 Conceptual Model
 4.3 Link to the Domain Model - 4439
 4.4 Link to the AP Module 439

4439

- Domain Model
- Mappings
 - Concept to Domain
 - Domain to Core
- CoreModel (CTC in scope of AP239)
- XSD
- XMI + XSLT

439

Modules ARM – AIM
 Mapping ARM to AIM
 Long form
 Normative Express

242 ed2

4.1 Activity Model
 4.2 Conceptual Model
 4.3 Link to the Domain Model - 4442
 4.4 Link to the AP Module 442

4442

- Domain Model
- Mappings
 - Concept to Domain
 - Domain to Core
- CoreModel (CTC in scope of AP242)
- XSD
- XMI + XSLT

442

Modules ARM – AIM
 Mapping ARM to AIM
 Long form
 Normative Express

1000

SMRL express

NEW SMRL

- CoreModel (all CTC)
- Parametric diagram CoreModel to SYSML ARM

4000

Proposed new STEP Architecture

